

SYSTEM, METHOD AND APPARATUS FOR AN INSTRUCTION DRIVEN DIGITAL VIDEO PROCESSOR

ABSTRACT OF THE DISCLOSURE

The present invention provides a system, method and an apparatus for a digital video processor comprising an error memory and a merge memory, a half pixel filter communicably coupled to the merge memory, a controller communicably coupled to the error memory, the merge memory and the half pixel filter. The present invention also including a sum unit communicably coupled to the error memory. The controller executing one or more instructions to provide motion compensation during video decoding.

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